

Model Name : GA-G41MT-ES2L**Revision 1.3****SHEET****TITLE**

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B,D
06	P4 LGA775 C
07	P4 LGA775 E,F,G,H
08	G41 HOST
09	G41 DDRII
10	G41 PCI E, DMI
11	G41 VGA
12	G41 GND
13	G41 PWR
14	PCI EXPRESS*16 SLOT
15	DDR3 CHANNEL A
16	DDR3 CHANNEL B
17	DDR3 TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	CK505 CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY
24	ITE 8718 GB
25	COM LPT
26	CI,HWM,KB/MS,DUALBIOS
27	ALC888B/888B-VD2

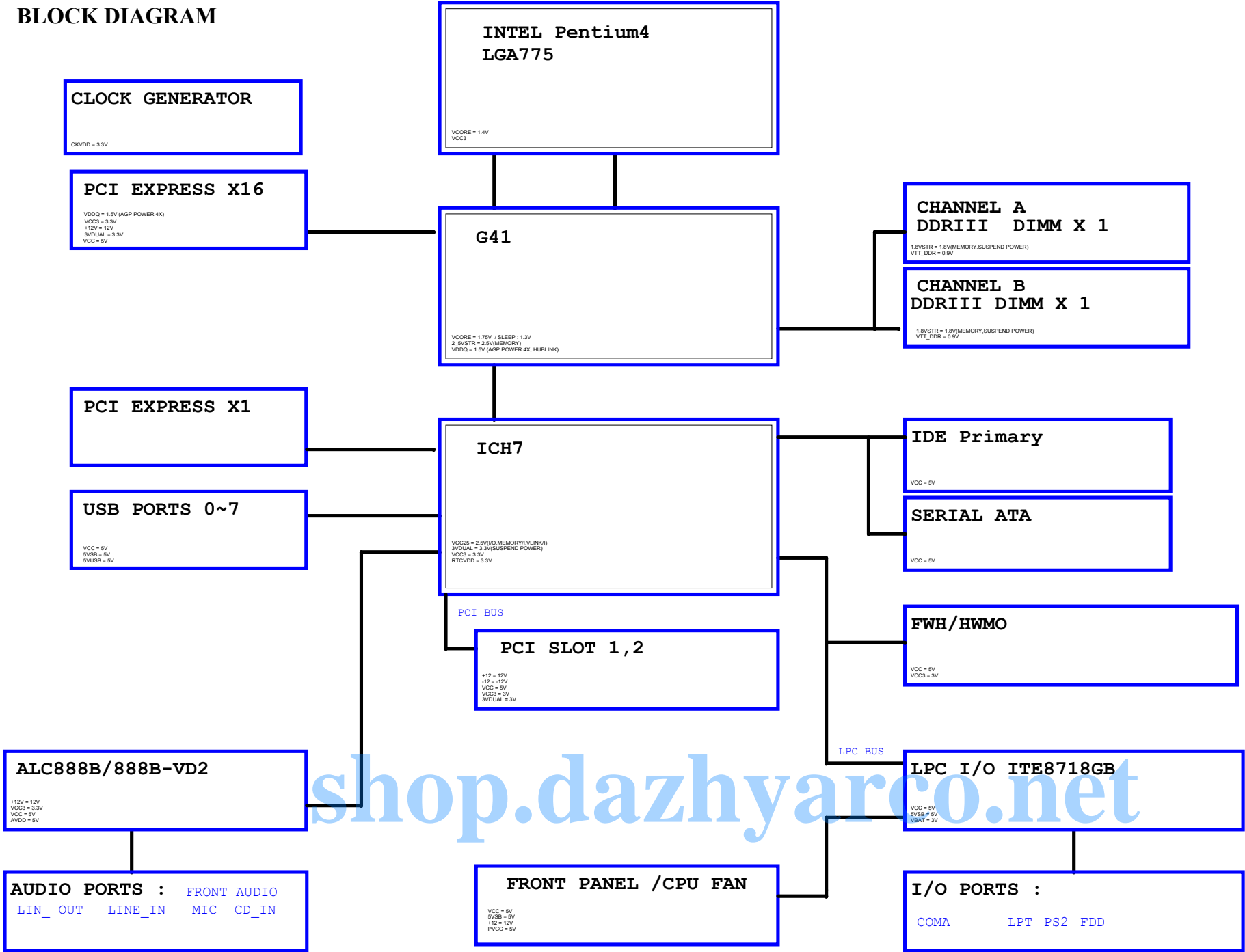
SHEET**TITLE**

28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM ISL6312
31	ATX, OTHERS POWER
32	FRONT PANEL
33	REALTEK RTL8111E/8105E

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Gigabyte Technology			
Title Cover Sheet			
Size Custom	Document Number GA-G41MT-ES2L	Rev 1.3	
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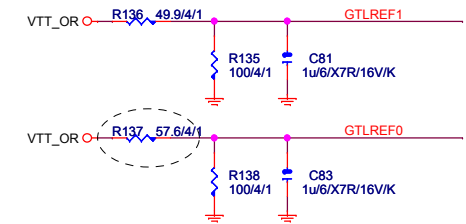
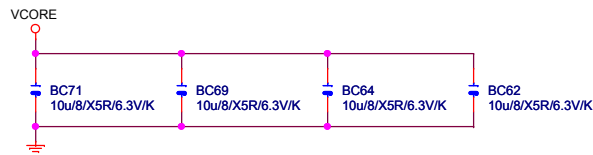
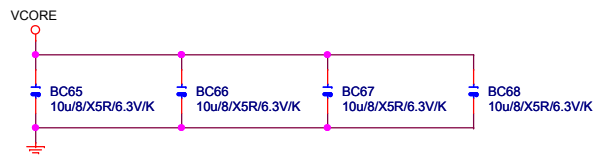
BLOCK DIAGRAM



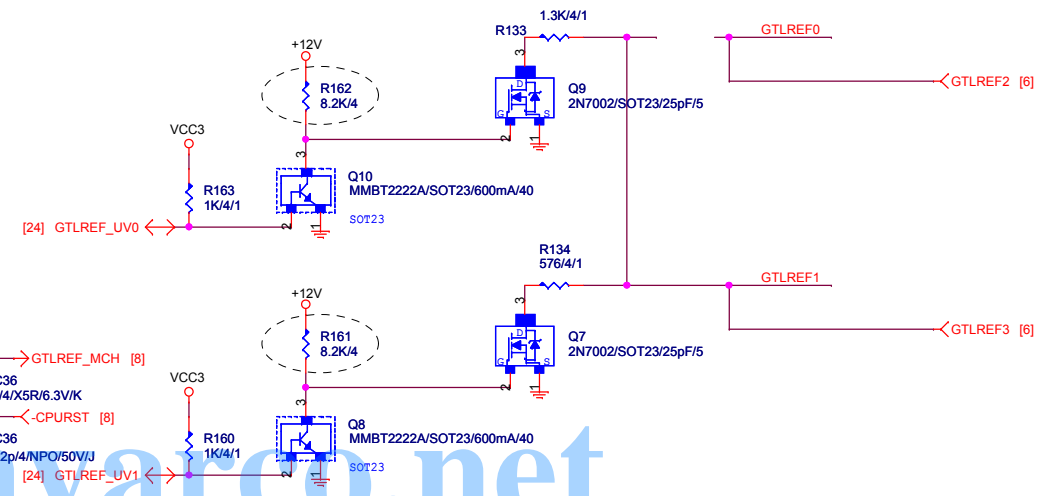
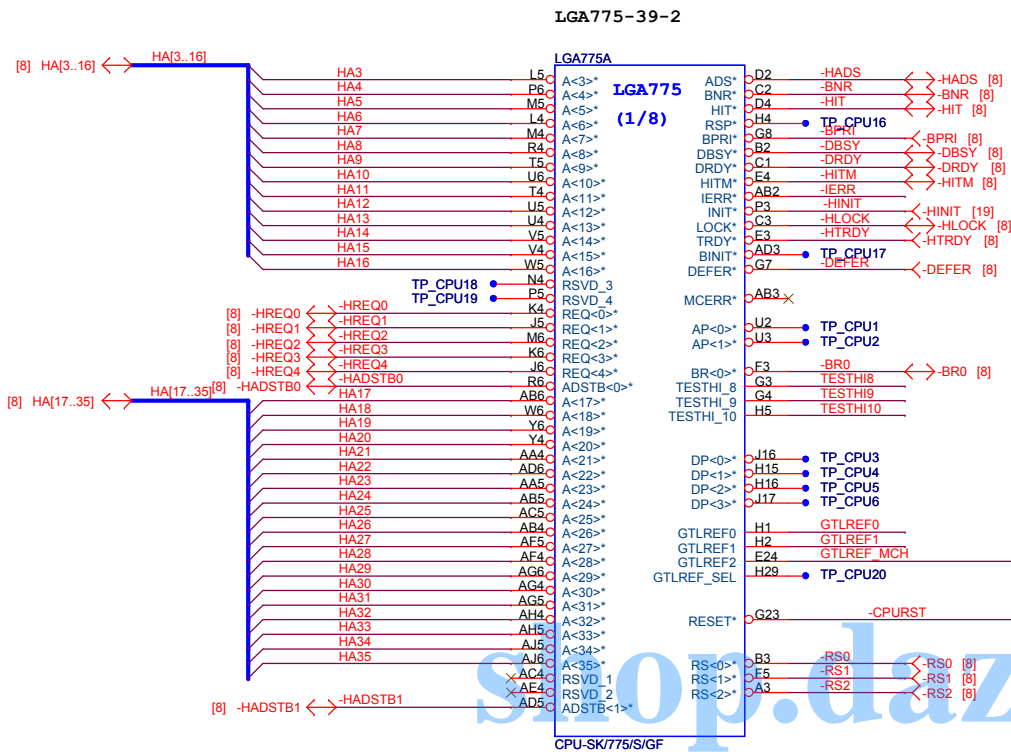
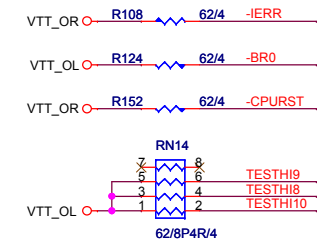
Version: 1.3

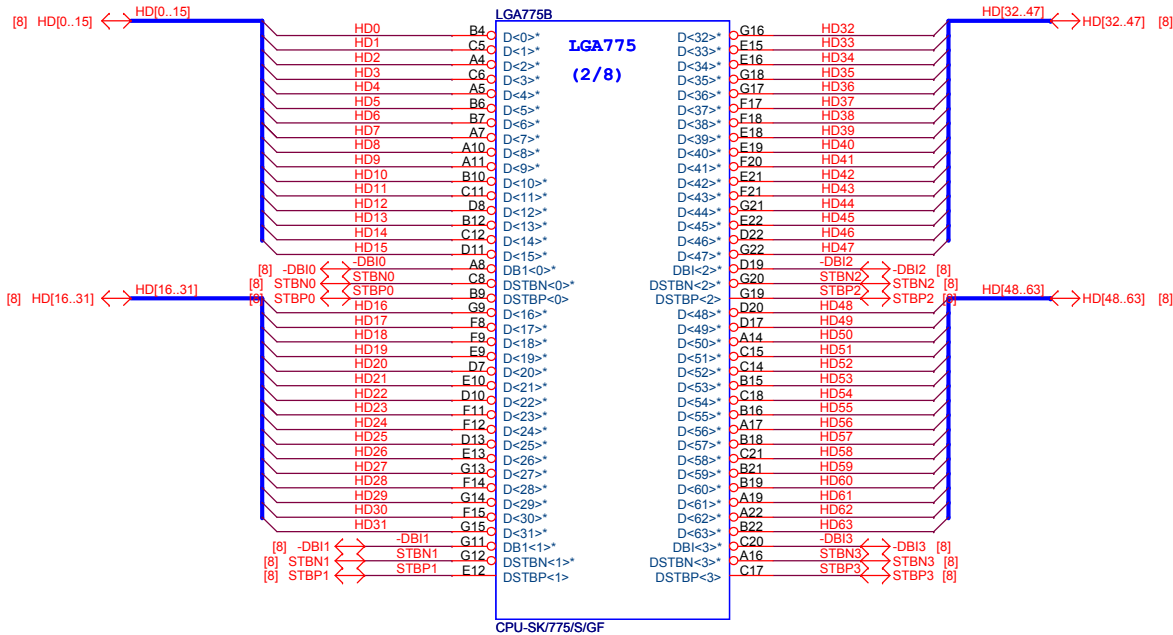
2010/04/15

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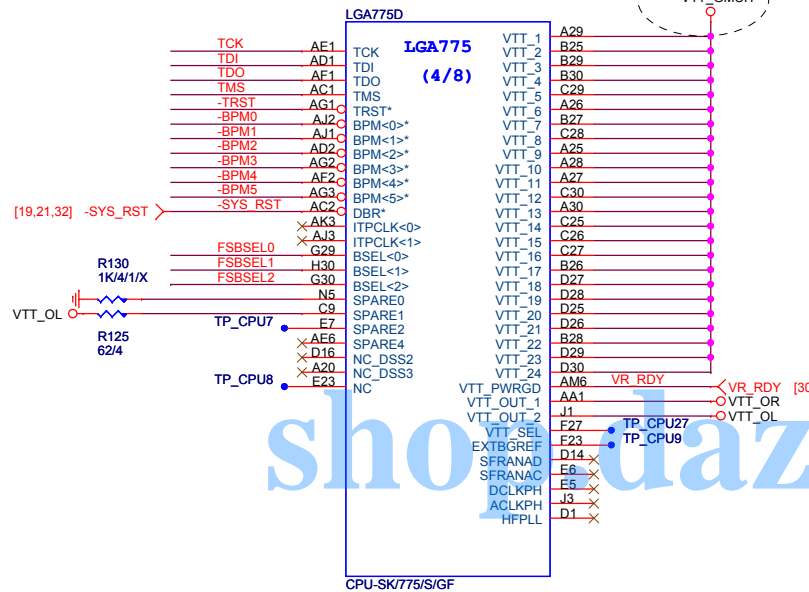


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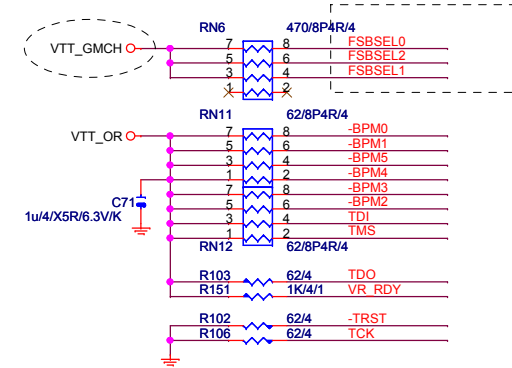




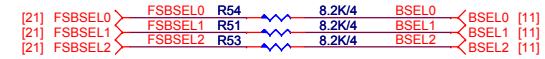
CPU-SK775/S/GF



CPU-SK775/S/GF



TO CLK GEN

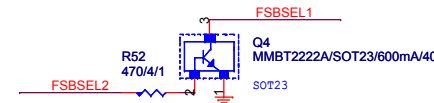


TO NB

CPU

NA	FSB	FSA	
FSBSEL3	FSBSEL1	FSBSEL0	Clock
1	0	1	100MHz
0	0	1	133MHz
0	1	1	166MHz
0	1	0	200MHz
0	0	0	266MHz

X



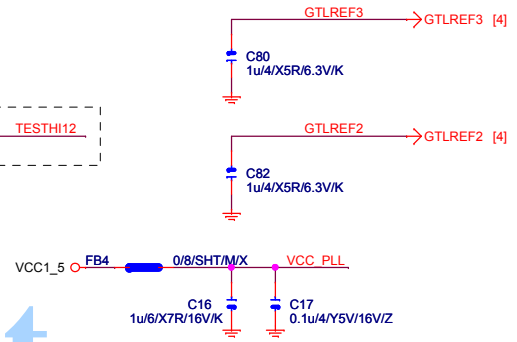
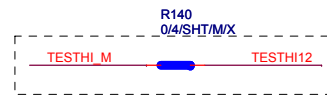
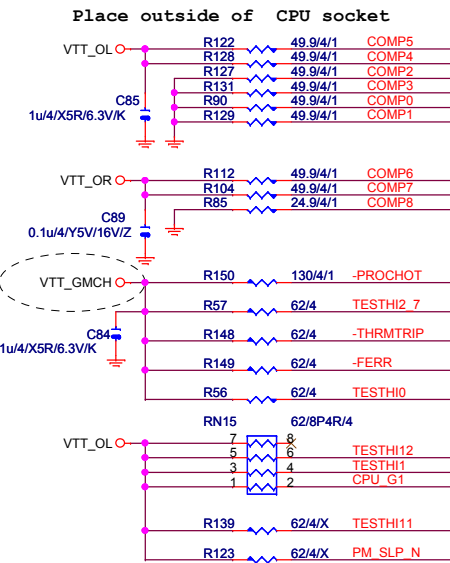
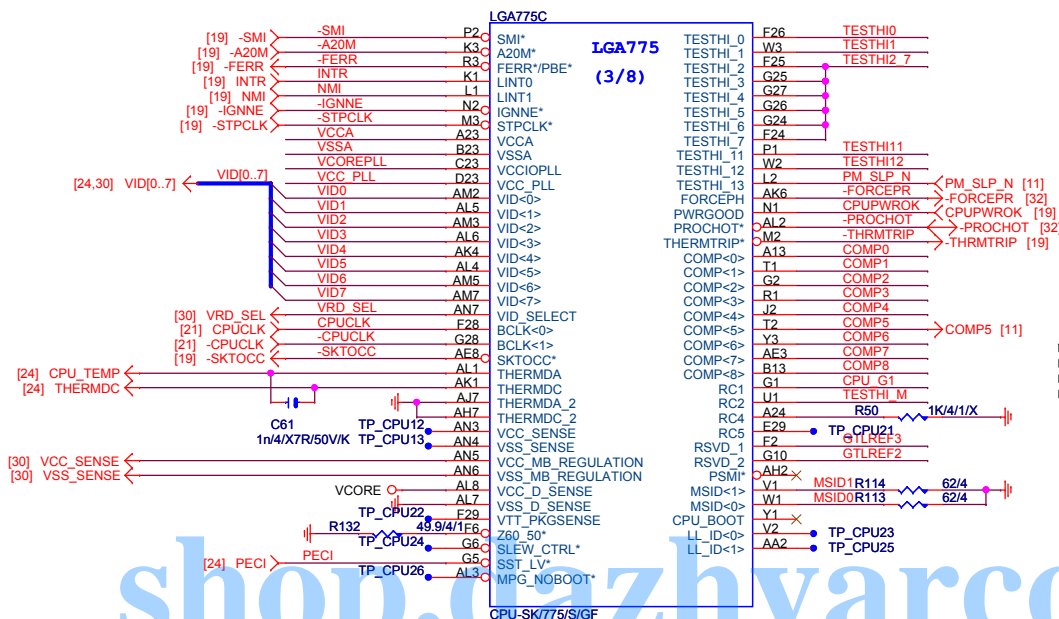
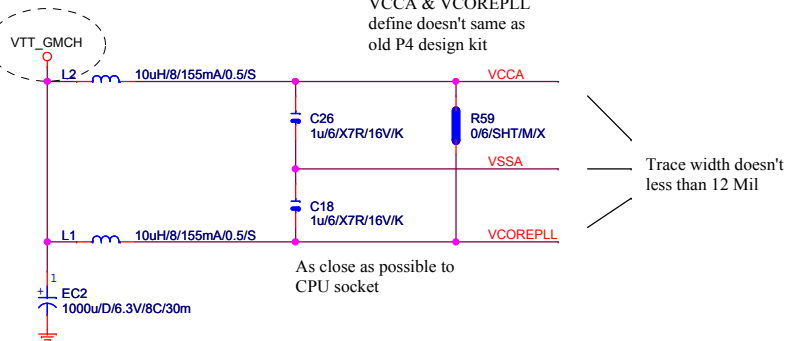
FIX FSB1600 LATCH FAIL

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Gigabyte Technology

Title			P4_LGA775-B,D
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Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



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PECI:Platform Environment Control Interface

Gigabyte Technology			
Title			
P4_LGA775-C			
Size	Document Number	Rev	
B	GA-G41MT-ES2L	1.3	
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CPU-SK/775/S/GF

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MCHC

EL
(3/9)
DDR_A

BC41	DDR_A_MA_0	BC5	DQSA0
MAAA1	DDR_A_MA_1	BD4	DQSA0
MAAA2	DDR_A_MA_2	BC3	DMA1
MAAA3	DDR_A_MA_3	BC2	MDA1
MAAA4	DDR_A_MA_4	BD3	MDA1
MAAA5	DDR_A_MA_5	BD7	MDA2
MAAA6	DDR_A_MA_6	BD7	MDA2
MAAA7	DDR_A_MA_7	BD7	MDA3
MAAA8	DDR_A_MA_8	BD2	MDA4
MAAA9	DDR_A_MA_9	BA3	MDA5
MAAA10	DDR_A_MA_10	BE6	MDA6
MAAA11	DDR_A_MA_11	BD6	MDA7
MAAA12	DDR_A_MA_12	BD9	DQSA1
MAAA13	DDR_A_MA_13	BD9	DQSA1
MAAA14	DDR_A_MA_14	BC9	DQSA1
AW42	DDR_A_WEB	BD9	DMA1
AW42	DDR_A_CASB	BD8	MDA8
AW42	DDR_A_RASB	BD8	MDA8
AW42	DDR_A_MA_9	BD11	MDA10
AW42	DDR_A_MA_10	BD11	MDA11
AW42	DDR_A_MA_11	BD11	MDA12
AW42	DDR_A_MA_12	BD11	MDA13
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AW42	DDR_A_MA_99	BD11	MDA100

G41A3[10H81-030G41-10R_10H81-030G41-20R]

MCHD

EL
(4/9)
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MAAB3	DDR_B_MA_3	MAAB3	DDR_B_MA_3
MAAB4	DDR_B_MA_4	MAAB4	DDR_B_MA_4
MAAB5	DDR_B_MA_5	MAAB5	DDR_B_MA_5
MAAB6	DDR_B_MA_6	MAAB6	DDR_B_MA_6
MAAB7	DDR_B_MA_7	MAAB7	DDR_B_MA_7
MAAB8	DDR_B_MA_8	MAAB8	DDR_B_MA_8
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MAAB71	DDR_B_MA_71	MAAB71	DDR_B_MA_71
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G41A3[10H81-030G41-10R_10H81-030G41-20R]

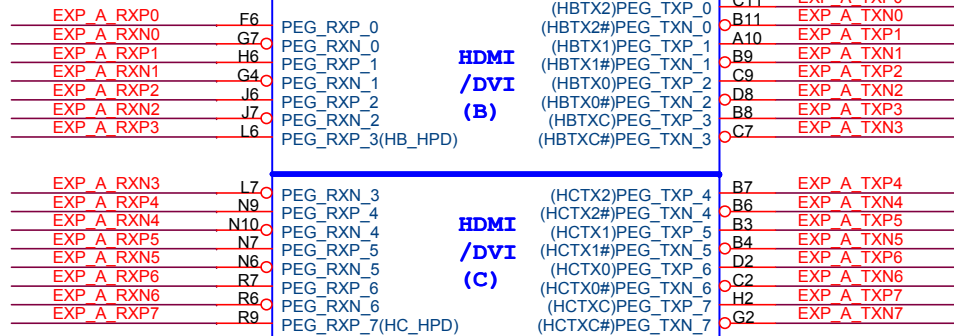
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DQSA0.7	DQSA0.7	[15]
DMA0.7	DMA0.7	[15]

MD0.63	MD0.63	[16]
MAAB0.14	MAAB0.14	[16]
MODT_B0.1	MODT_B0.1	[16]
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DQSB0.7	DQSB0.7	[16]
DMB0.7	DMB0.7	[16]

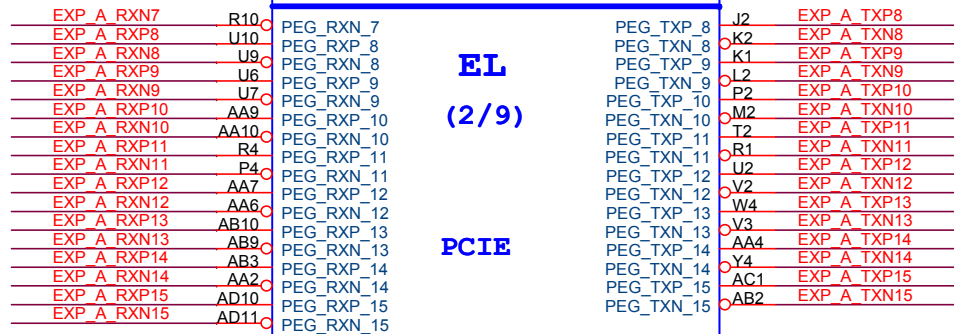
Gigabyte Technology

Title			Rev
GMCH-MEMORY			1.3
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C			
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MCHB



EXP_A_TXP[0..15] >>> EXP_A_TXP[0..15] [14]
EXP_A_TXN[0..15] >>> EXP_A_TXN[0..15] [14]
EXP_A_RXP[0..15] >>> EXP_A_RXP[0..15] [14]
EXP_A_RXN[0..15] >>> EXP_A_RXN[0..15] [14]



Close to MCH

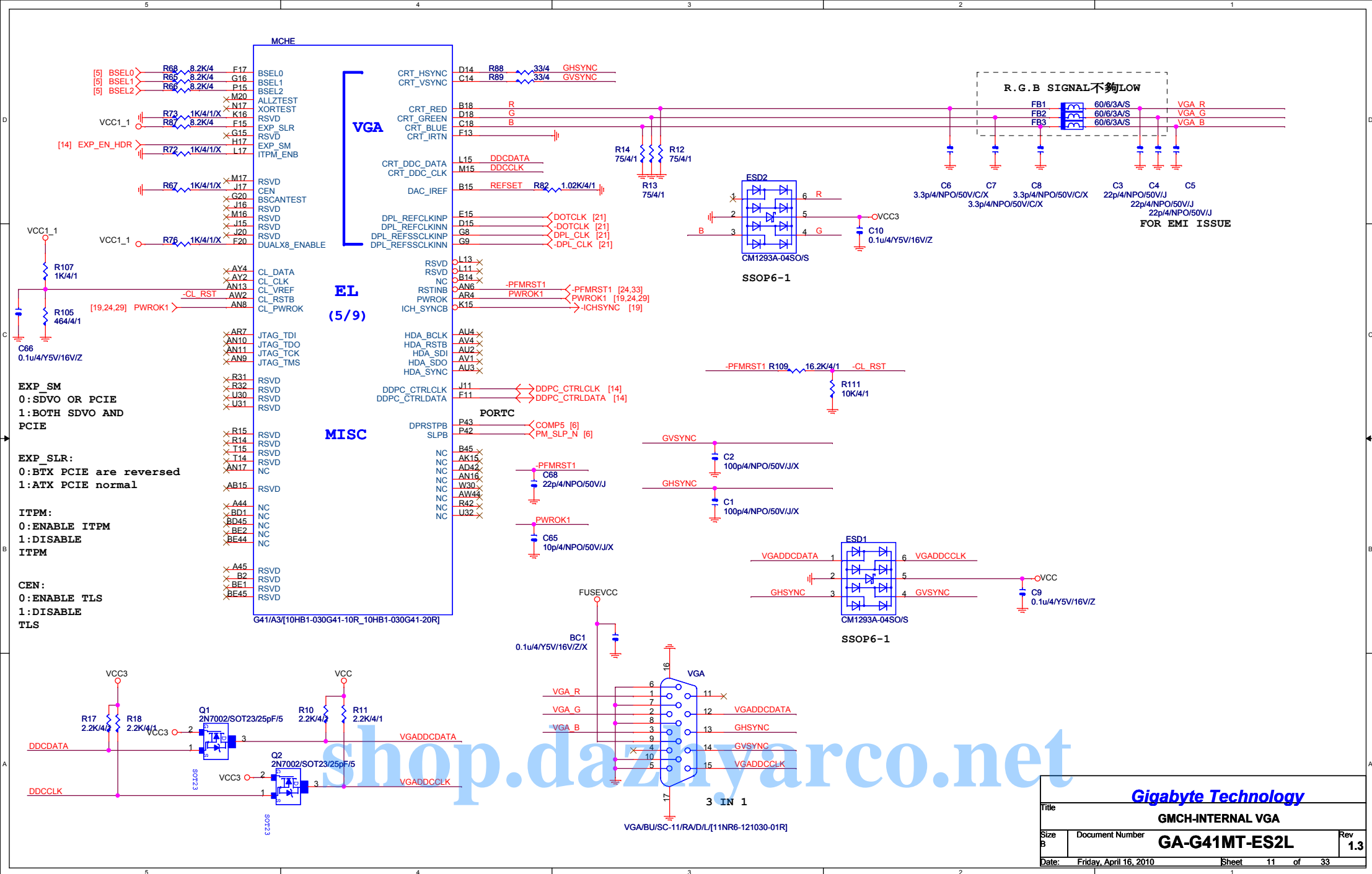


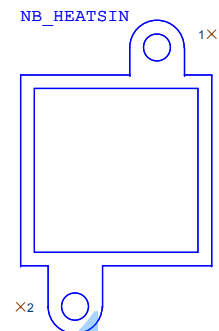
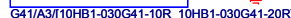
PORTB

G41/A3/[10HB1-030G41-10R_10HB1-030G41-20R]

Gigabyte Technology

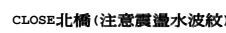
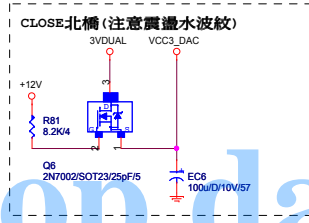
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GMCH-PCI E & DMI			
Size	Document Number	Rev	
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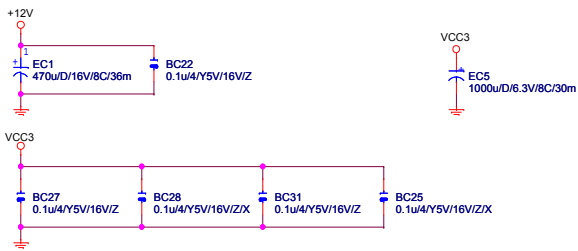




BGASINK445A-L GRAY 20MM

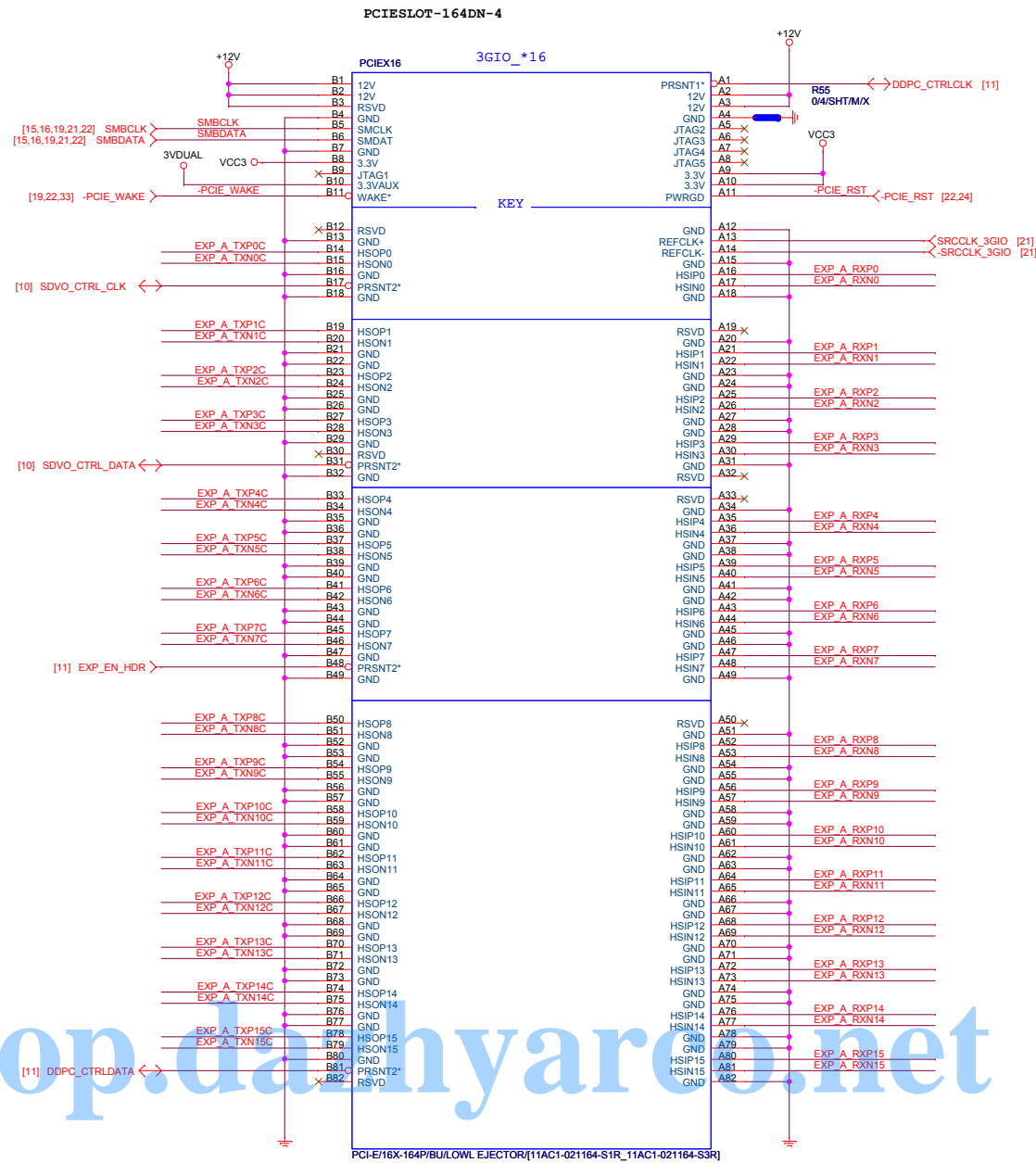
<i>Gigabyte Technology</i>				
GMCH-GND				
Title				Rev
Size Custom	Document Number	GA-G41MT-ES2L		1.3
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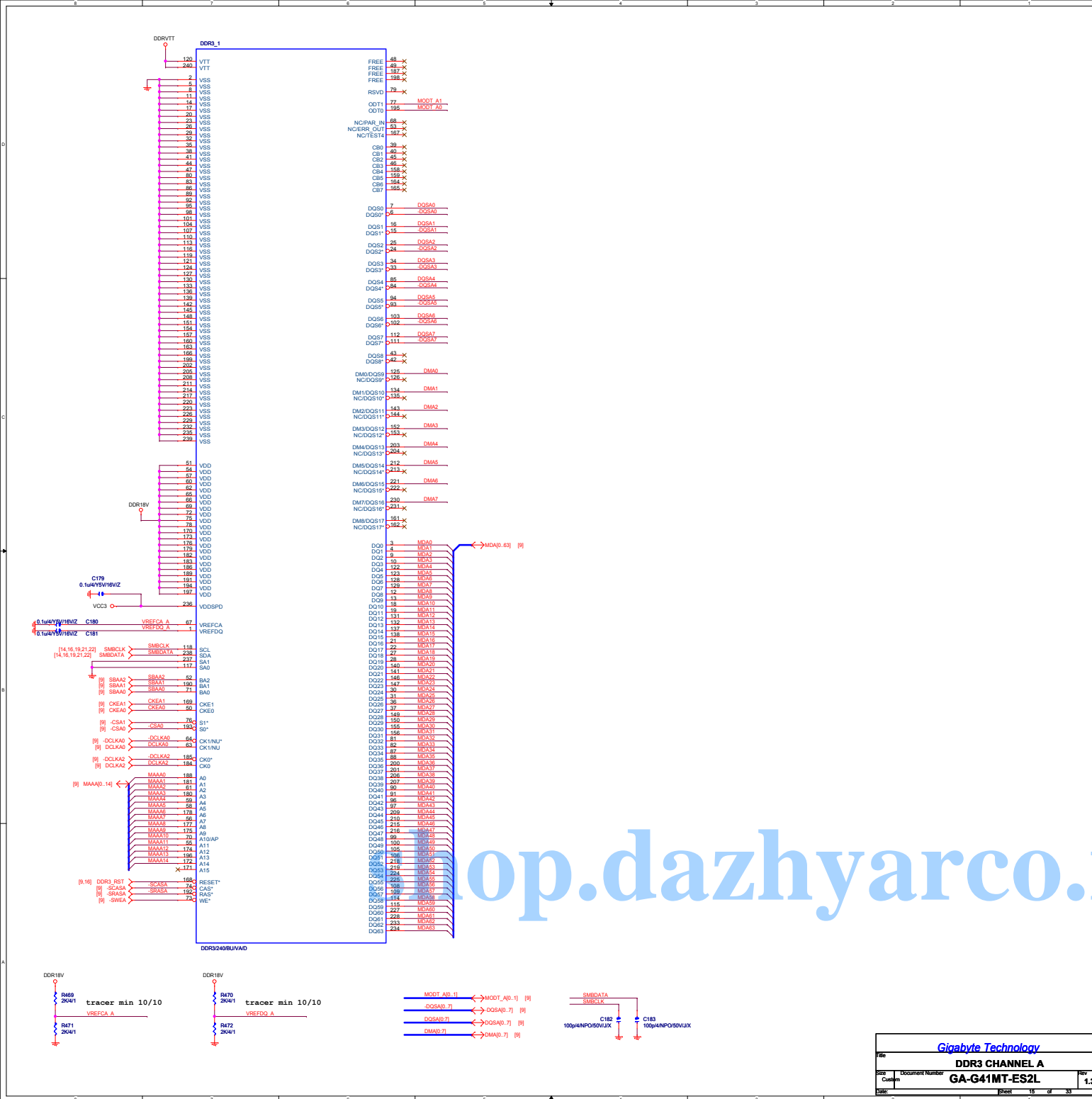


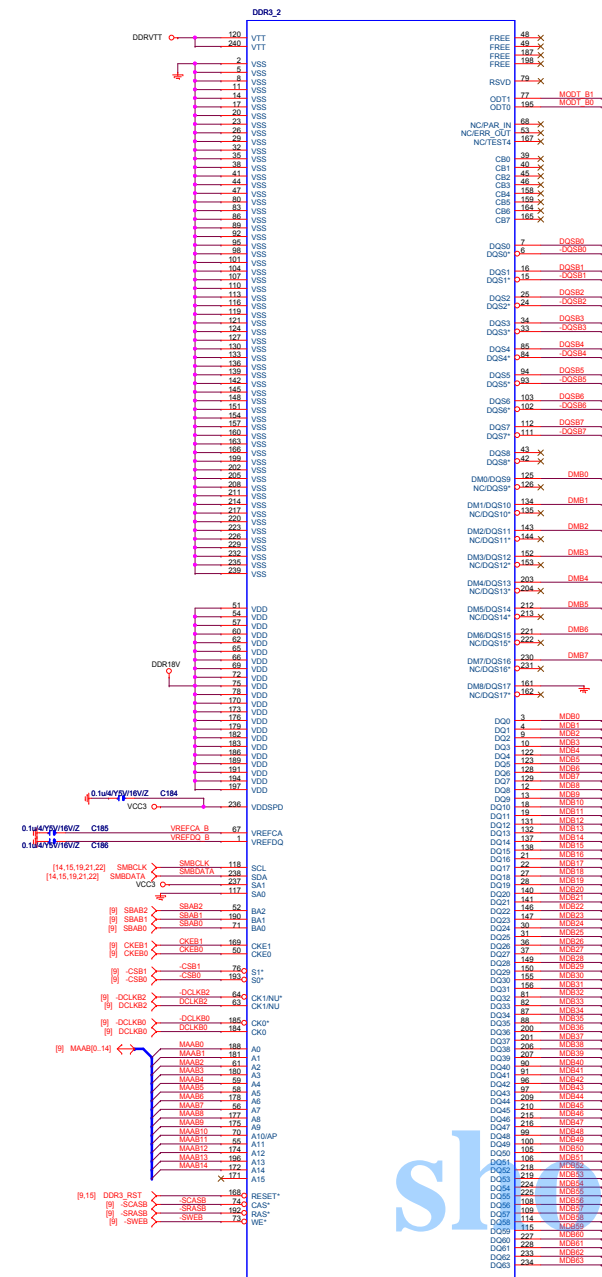
EXP_A_TXP[0..15] >>> EXP_A_TXP[0..15] [10] EXP_A_RXP[0..15] >>> EXP_A_RXP[0..15] [10]
EXP_A_TXN[0..15] >>> EXP_A_TXN[0..15] [10] EXP_A_RXN[0..15] >>> EXP_A_RXN[0..15] [10]

EXP_A_TXP0	C30	0.1uF/4X7R/16V/K	EXP_A_TXP0C
EXP_A_TXN0	C29	0.1uF/4X7R/16V/K	EXP_A_TXN0C
EXP_A_TXP1	C31	0.1uF/4X7R/16V/K	EXP_A_TXP1C
EXP_A_TXN1	C32	0.1uF/4X7R/16V/K	EXP_A_TXN1C
EXP_A_TXP2	C33	0.1uF/4X7R/16V/K	EXP_A_TXP2C
EXP_A_TXN2	C34	0.1uF/4X7R/16V/K	EXP_A_TXN2C
EXP_A_TXP3	C37	0.1uF/4X7R/16V/K	EXP_A_TXP3C
EXP_A_TXN3	C38	0.1uF/4X7R/16V/K	EXP_A_TXN3C
EXP_A_TXP4	C39	0.1uF/4X7R/16V/K	EXP_A_TXP4C
EXP_A_TXN4	C40	0.1uF/4X7R/16V/K	EXP_A_TXN4C
EXP_A_TXP5	C42	0.1uF/4X7R/16V/K	EXP_A_TXP5C
EXP_A_TXN5	C41	0.1uF/4X7R/16V/K	EXP_A_TXN5C
EXP_A_TXP6	C44	0.1uF/4X7R/16V/K	EXP_A_TXP6C
EXP_A_TXN6	C43	0.1uF/4X7R/16V/K	EXP_A_TXN6C
EXP_A_TXP7	C47	0.1uF/4X7R/16V/K	EXP_A_TXP7C
EXP_A_TXN7	C45	0.1uF/4X7R/16V/K	EXP_A_TXN7C
EXP_A_TXP8	C48	0.1uF/4X7R/16V/K	EXP_A_TXP8C
EXP_A_TXN8	C49	0.1uF/4X7R/16V/K	EXP_A_TXN8C
EXP_A_TXP9	C59	0.1uF/4X7R/16V/K	EXP_A_TXP9C
EXP_A_TXN9	C60	0.1uF/4X7R/16V/K	EXP_A_TXN9C
EXP_A_TXP10	C63	0.1uF/4X7R/16V/K	EXP_A_TXP10C
EXP_A_TXN10	C62	0.1uF/4X7R/16V/K	EXP_A_TXN10C
EXP_A_TXP11	C67	0.1uF/4X7R/16V/K	EXP_A_TXP11C
EXP_A_TXN11	C64	0.1uF/4X7R/16V/K	EXP_A_TXN11C
EXP_A_TXP12	C69	0.1uF/4X7R/16V/K	EXP_A_TXP12C
EXP_A_TXN12	C70	0.1uF/4X7R/16V/K	EXP_A_TXN12C
EXP_A_TXP13	C73	0.1uF/4X7R/16V/K	EXP_A_TXP13C
EXP_A_TXN13	C72	0.1uF/4X7R/16V/K	EXP_A_TXN13C
EXP_A_TXP14	C76	0.1uF/4X7R/16V/K	EXP_A_TXP14C
EXP_A_TXN14	C74	0.1uF/4X7R/16V/K	EXP_A_TXN14C
EXP_A_TXP15	C78	0.1uF/4X7R/16V/K	EXP_A_TXP15C
EXP_A_TXN15	C77	0.1uF/4X7R/16V/K	EXP_A_TXN15C



LOWV LEFT BLUE

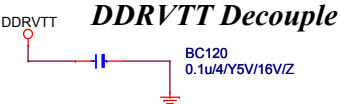
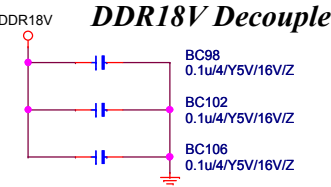
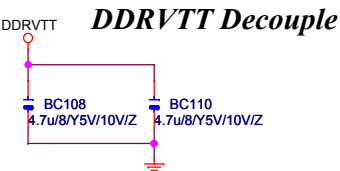
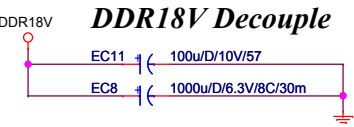




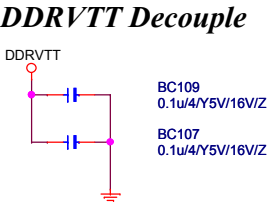
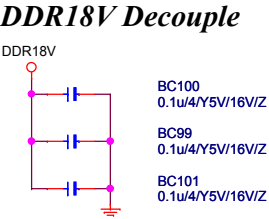
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DDR TERMINATION
CHANNEL A



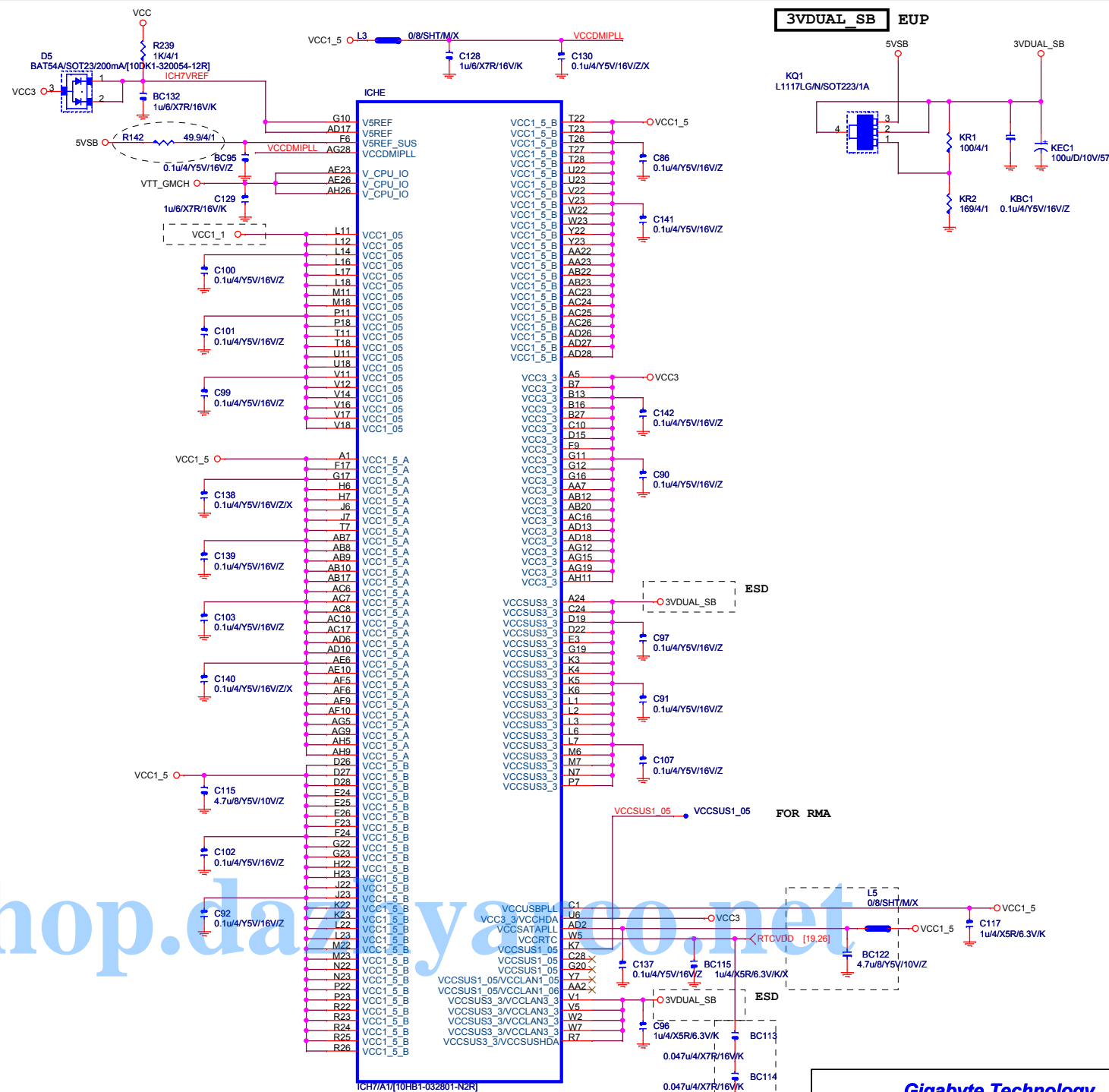
DDR TERMINATION
CHANNEL B

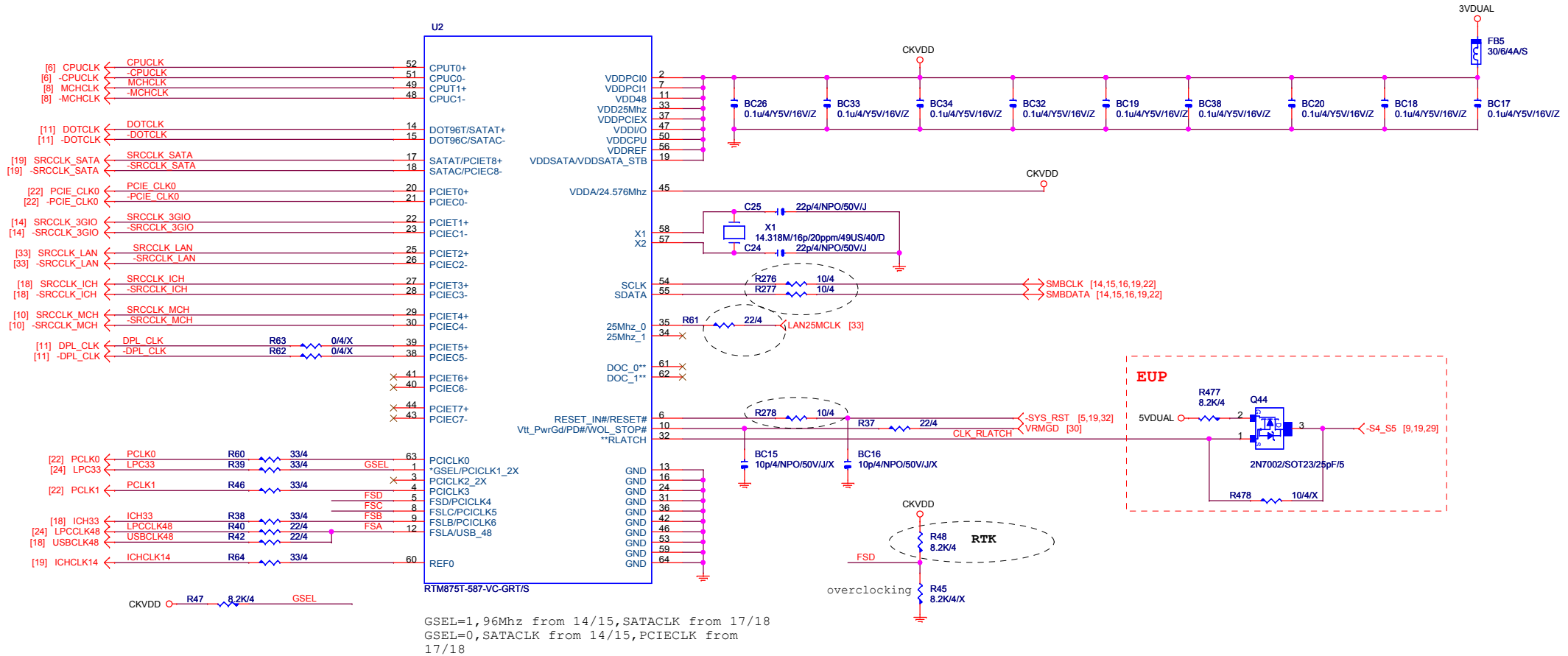


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Gigabyte Technology			
Title			
DDRII TERMINATOR			
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ICHF			
A4	VSS1	VSS101	R14
A23	VSS2	VSS102	R15
B1	VSS3	VSS103	R16
B8	VSS4	VSS104	R17
B11	VSS5	VSS105	R18
B14	VSS6	VSS106	T8
B17	VSS7	VSS107	T12
B20	VSS8	VSS108	T13
B26	VSS9	VSS109	T14
B28	VSS10	VSS110	T15
C2	VSS11	VSS111	T16
C6	VSS12	VSS112	T17
D10	VSS13	VSS113	U4
D13	VSS14	VSS114	U12
D18	VSS15	VSS115	U13
D21	VSS16	VSS116	U14
D24	VSS17	VSS117	U15
E1	VSS18	VSS118	U16
E2	VSS19	VSS119	U17
E8	VSS20	VSS120	U25
E15	VSS21	VSS121	U26
F3	VSS22	VSS122	V2
F4	VSS23	VSS123	V13
F5	VSS24	VSS124	V15
F12	VSS25	VSS125	V24
F27	VSS26	VSS126	V27
F28	VSS27	VSS127	V28
G1	VSS28	VSS128	W6
G2	VSS29	VSS129	W24
G5	VSS30	VSS130	W25
G6	VSS31	VSS131	W26
G9	VSS32	VSS132	Y3
G14	VSS33	VSS133	Y24
G18	VSS34	VSS134	Y27
G21	VSS35	VSS135	Y28
G24	VSS36	VSS136	AA1
G25	VSS37	VSS137	AA24
G26	VSS38	VSS138	AA25
H3	VSS39	VSS139	AA26
H4	VSS40	VSS140	AB4
H24	VSS41	VSS141	AB6
H27	VSS43	VSS143	AB11
H28	VSS44	VSS144	AB14
J1	VSS45	VSS145	AB16
J5	VSS46	VSS146	AB19
J24	VSS47	VSS147	AB21
J25	VSS48	VSS148	AB24
J26	VSS49	VSS149	AB27
K24	VSS50	VSS150	AB28
K27	VSS51	VSS151	AC2
K28	VSS52	VSS152	AC5
L13	VSS53	VSS153	AC9
L15	VSS54	VSS154	AC11
L24	VSS55	VSS155	AD1
L25	VSS56	VSS156	AD3
L26	VSS57	VSS157	AD4
M3	VSS58	VSS158	AD7
M4	VSS59	VSS159	AD8
M5	VSS60	VSS160	AD11
M12	VSS61	VSS161	AD15
M13	VSS62	VSS162	AD19
M14	VSS63	VSS163	AD23
M15	VSS64	VSS164	AE2
M16	VSS65	VSS165	AE4
M17	VSS66	VSS166	AE8
M24	VSS67	VSS167	AE11
M27	VSS68	VSS168	AE13
M28	VSS69	VSS169	AE18
N1	VSS70	VSS170	AE21
N2	VSS71	VSS171	AE24
N5	VSS72	VSS172	AE25
N6	VSS73	VSS173	AF2
N11	VSS74	VSS174	AF8
N12	VSS75	VSS175	AF11
N13	VSS76	VSS176	AF27
N14	VSS77	VSS177	AF28
N15	VSS78	VSS178	AG1
N16	VSS79	VSS179	AG3
N17	VSS80	VSS180	AG7
N18	VSS81	VSS181	AG14
N24	VSS82	VSS182	AG17
N25	VSS83	VSS183	AG20
N26	VSS84	VSS184	AG24
P3	VSS85	VSS185	AH1
P4	VSS86	VSS186	AH3
P12	VSS87	VSS187	AH7
P13	VSS88	VSS188	AH12
P14	VSS89	VSS189	AH23
P15	VSS90	VSS190	AH27
P16	VSS91	VSS191	C27
P17	VSS92	VSS192	E4
P24	VSS93	VSS193	AG11
P27	VSS94	VSS194	
P28	VSS95		
P29	VSS96		
R1	VSS97		
R11	VSS98		
R12	VSS99		
R13	VSS100		





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Title			CK505 CLK GEN
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The schematic diagram shows the LED driver circuit for the CD4148WP. It includes a 5V regulator (D7) connected to a 100k resistor (R228) and a 10k resistor (R229) to ground. The output of the regulator is connected to the anode of the LED (HDLED) and the collector of the PNP transistor (Q21). The emitter of Q21 is connected to ground. The base of Q21 is connected to the wiper of a 10k potentiometer (R227) and the collector of the NPN transistor (Q28). The emitter of Q28 is connected to ground. The base of Q28 is connected to the wiper of a 10k potentiometer (R229) and the collector of the PNP transistor (Q21). The circuit is powered by a 5V regulator (D7) connected to a 100k resistor (R228) and a 10k resistor (R229) to ground. The output of the regulator is connected to the anode of the LED (HDLED) and the collector of the PNP transistor (Q21). The emitter of Q21 is connected to ground. The base of Q21 is connected to the wiper of a 10k potentiometer (R227) and the collector of the NPN transistor (Q28). The emitter of Q28 is connected to ground. The base of Q28 is connected to the wiper of a 10k potentiometer (R229) and the collector of the PNP transistor (Q21).

[illegible]

The schematic diagram illustrates the internal structure of the SSOP6-1 package, which contains a USB connector and a fuse. The USB connector is labeled 'USB1' and has pins 1 through 6. The fuse is labeled 'FUSEVCC1' and is connected to the VCC pin of the USB connector. The package is labeled 'SSOP6-1'.

The USB connector pins are connected to the following signals:

- Pins 1 and 2: +USBP0, -USBP0
- Pins 3 and 4: +USBP1, -USBP1
- Pins 5 and 6: +USBP2, -USBP2

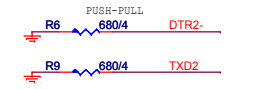
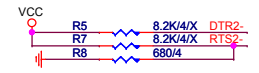
The fuse is connected to the VCC pin of the USB connector and is labeled 'FUSEVCC1'.

The package is labeled 'SSOP6-1'.

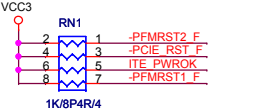
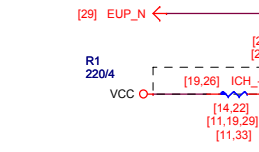
[illegible]

Title			
IDE,FDD,F_USB,R_USB			
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RTS2- ==LOW CPU FAN 50%
==HIGH 100%
DEFAULT 50%

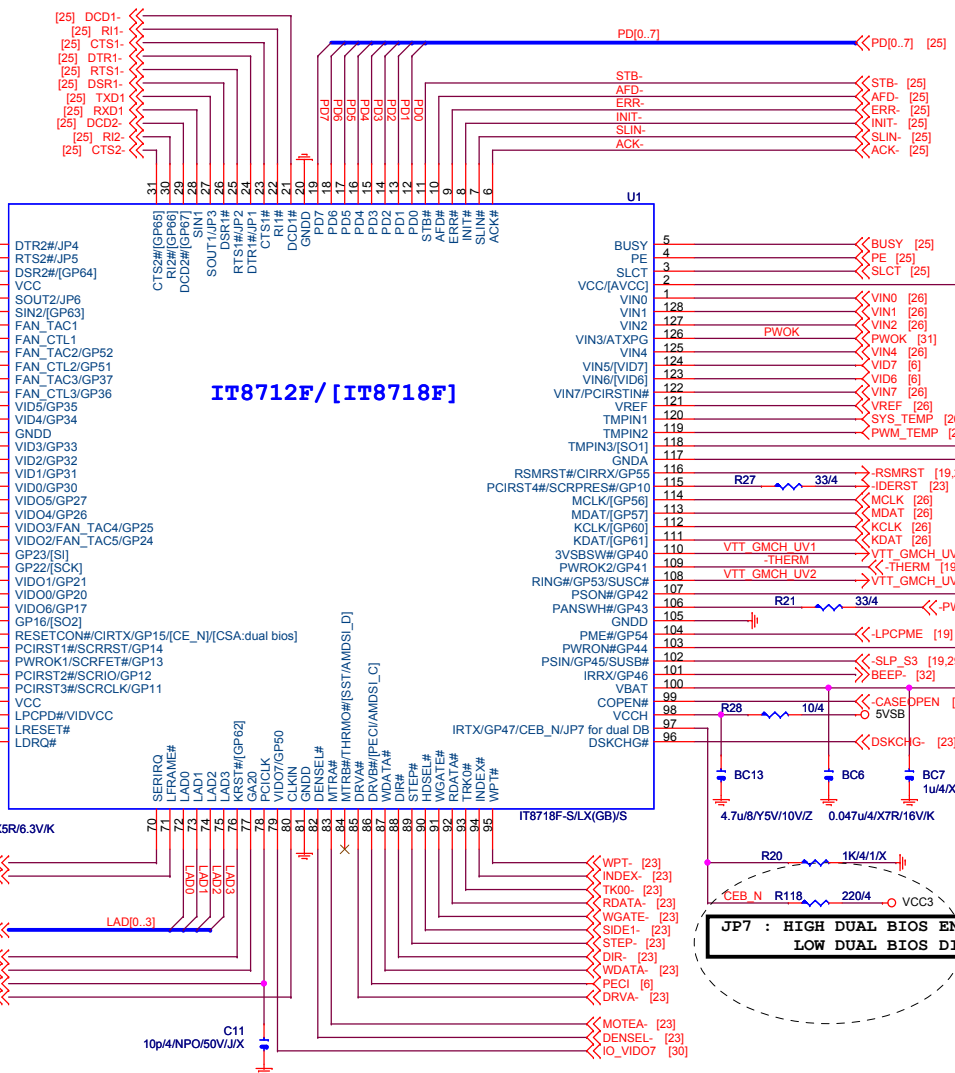


EUP

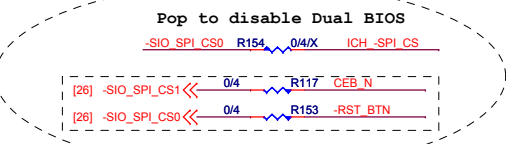


Dual BIOS:
GB logo :Pin 61 (GP15/CSA)
GB logo :Pin 59 (GP17/CSB)
Pin 59 Dual BIOS ,Power On Strapping:
H ==>Dual BIOS function Enable
L ==>Dual BIOS function Disable

1.2V or 3.3V tolerance select.
1.2V OUTPUT 接 VTT_GMCH
3.3V OUTPUT 接 3.3V
LPCPD# =VIDVCC



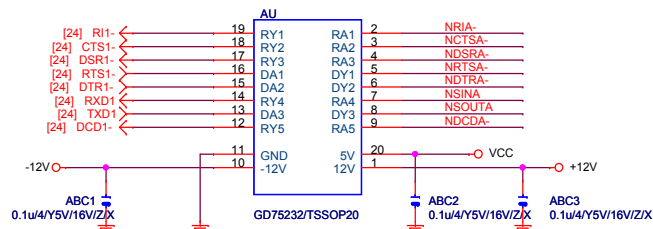
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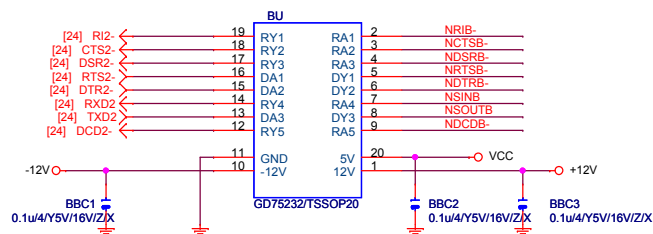
JP7 : HIGH DUAL BIOS ENABLE
LOW DUAL BIOS DISABLE

<i>Gigabyte Technology</i>			
Title			
ITE 8712/18 LPC IO			
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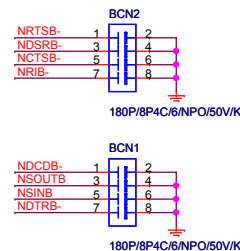
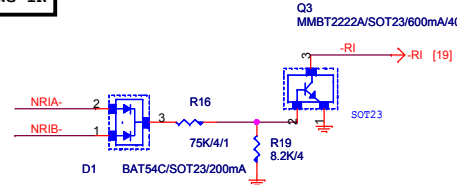
COMA



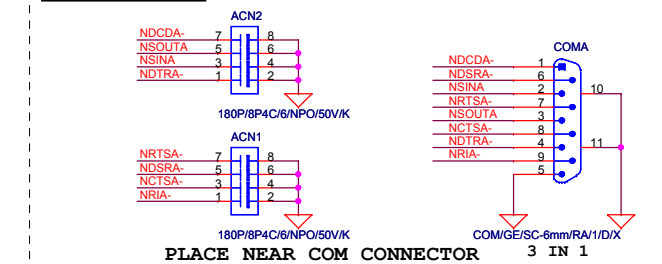
IC20TSSOP-1



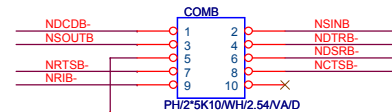
RING IN



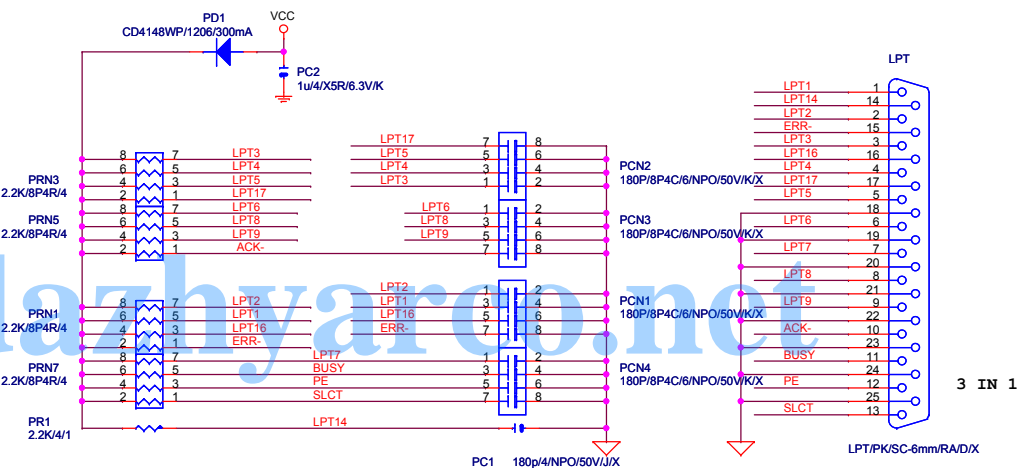
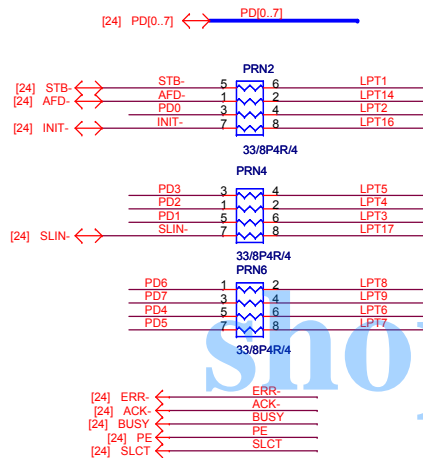
EXTERNAL COMA



PLACE NEAR COM CONNECTOR



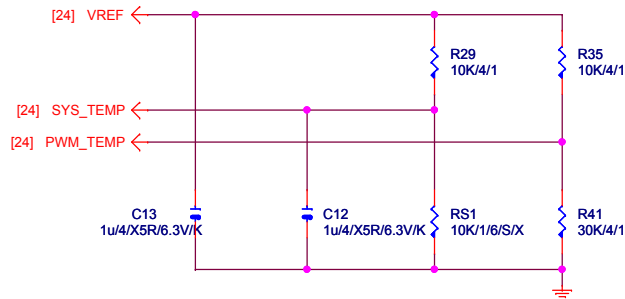
LPT PORT



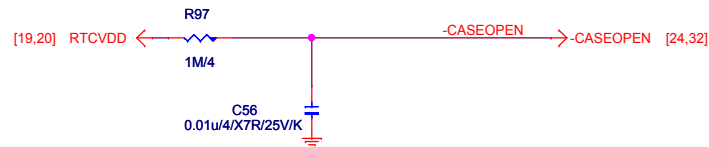
Gigabyte Technology

Title			COM & LPT PORT
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TEMP H/W MONITOR

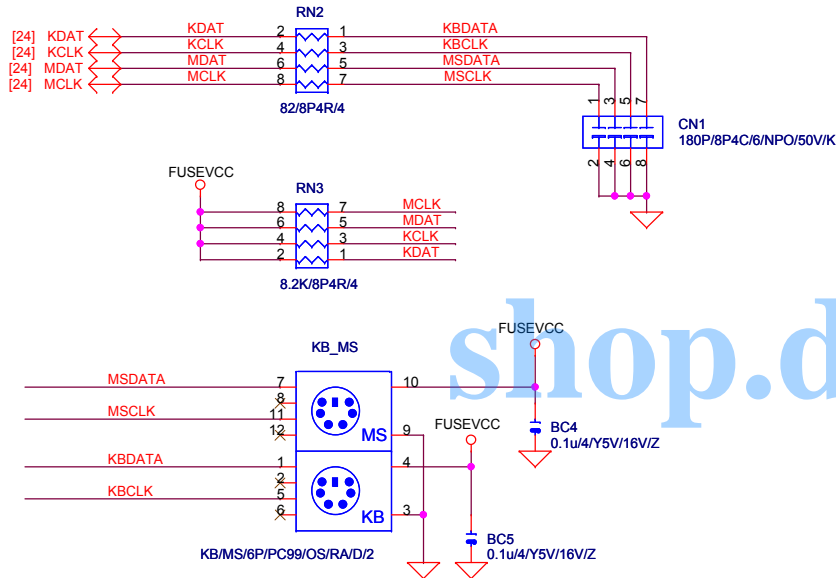


CASE OPEN

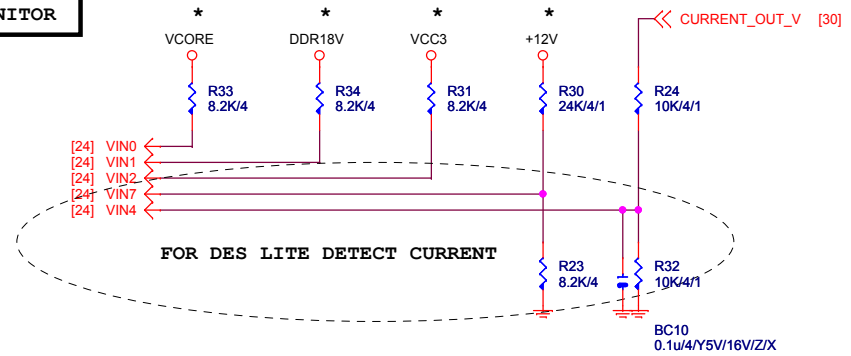


Case Open Circuits

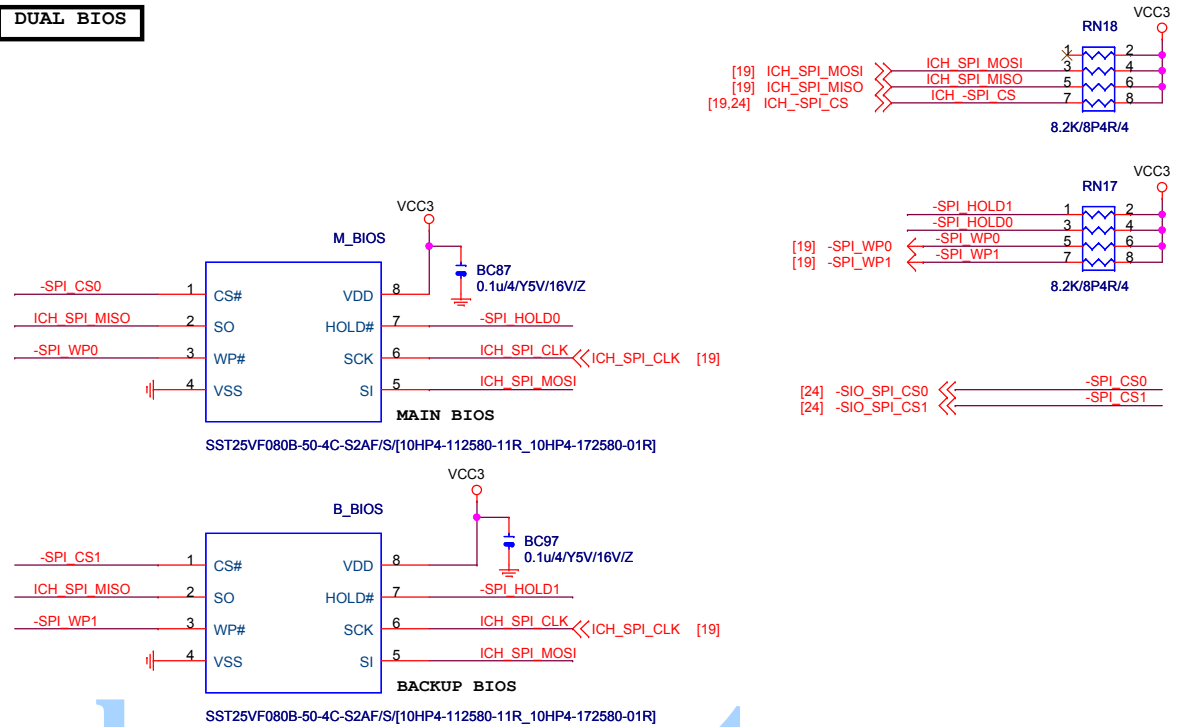
KB/MS



VOLTAGE-- H/W MONITOR



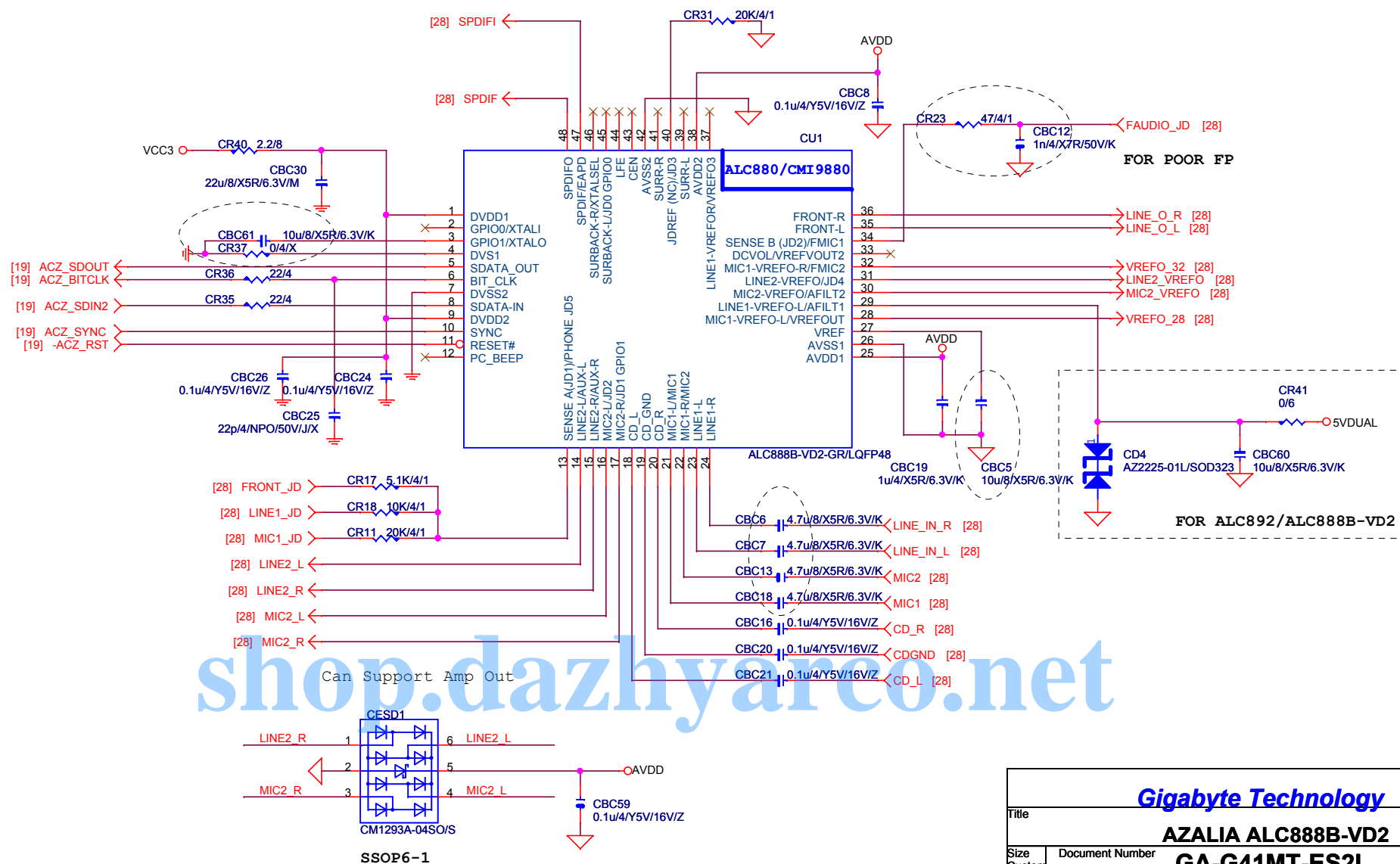
DUAL BIOS



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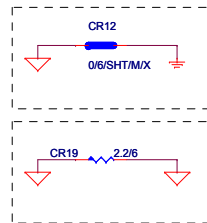
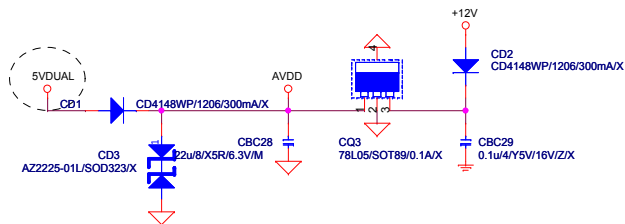
Title			HW-MONITOR/CI/KB/MS/BIOS	
Size	Custom	Document Number	GA-G41MT-ES2L	
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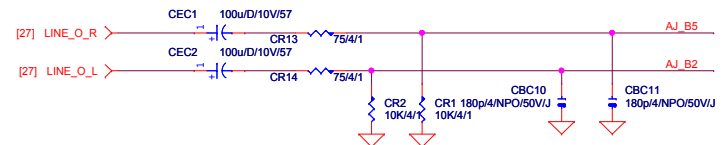
Gigabyte Technology

Title					AZALIA ALC888B-VD2					Rev 1.3	
Size Custom		Document Number GA-G41MT-ES2L									
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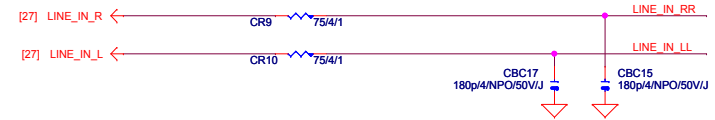
CODEC POWER/EMI PAD



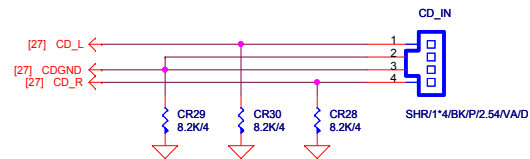
LINE-OUT



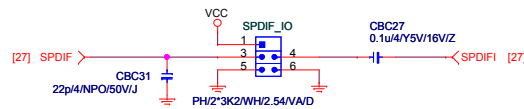
LINE-IN



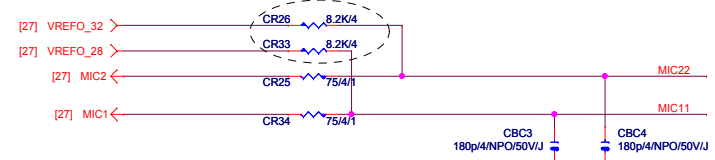
CD IN



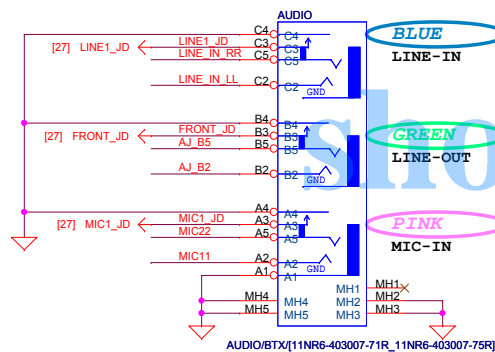
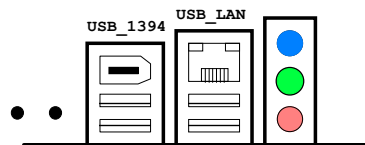
SPDIF IN



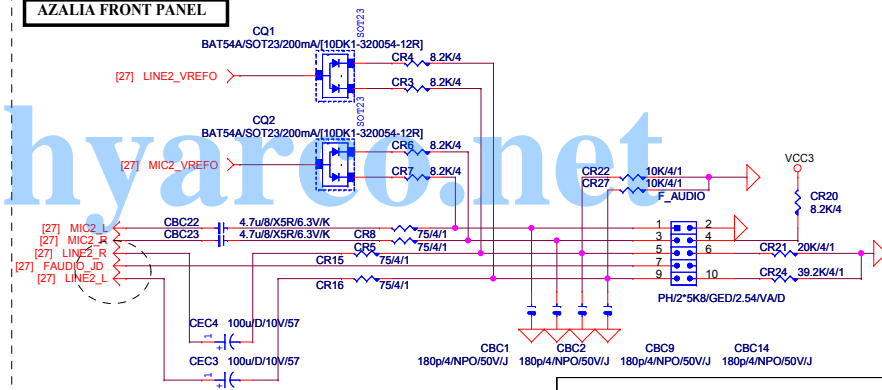
MIC-IN



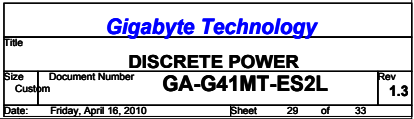
AZALIA JACK

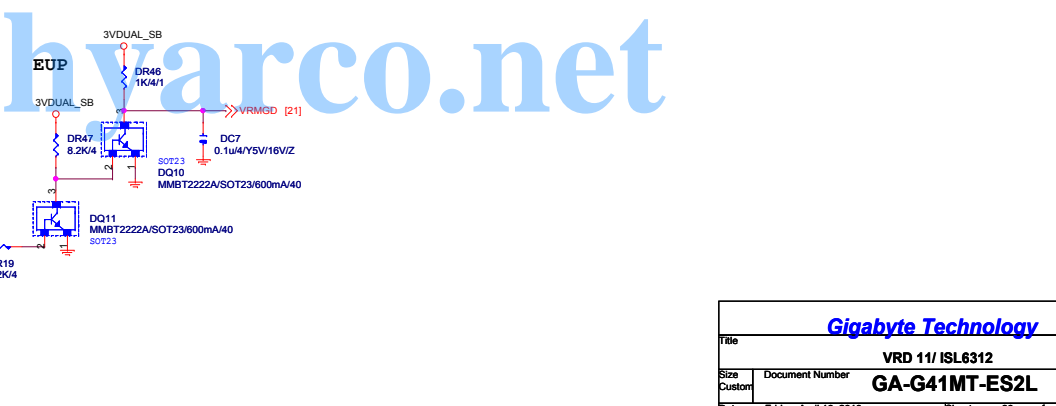
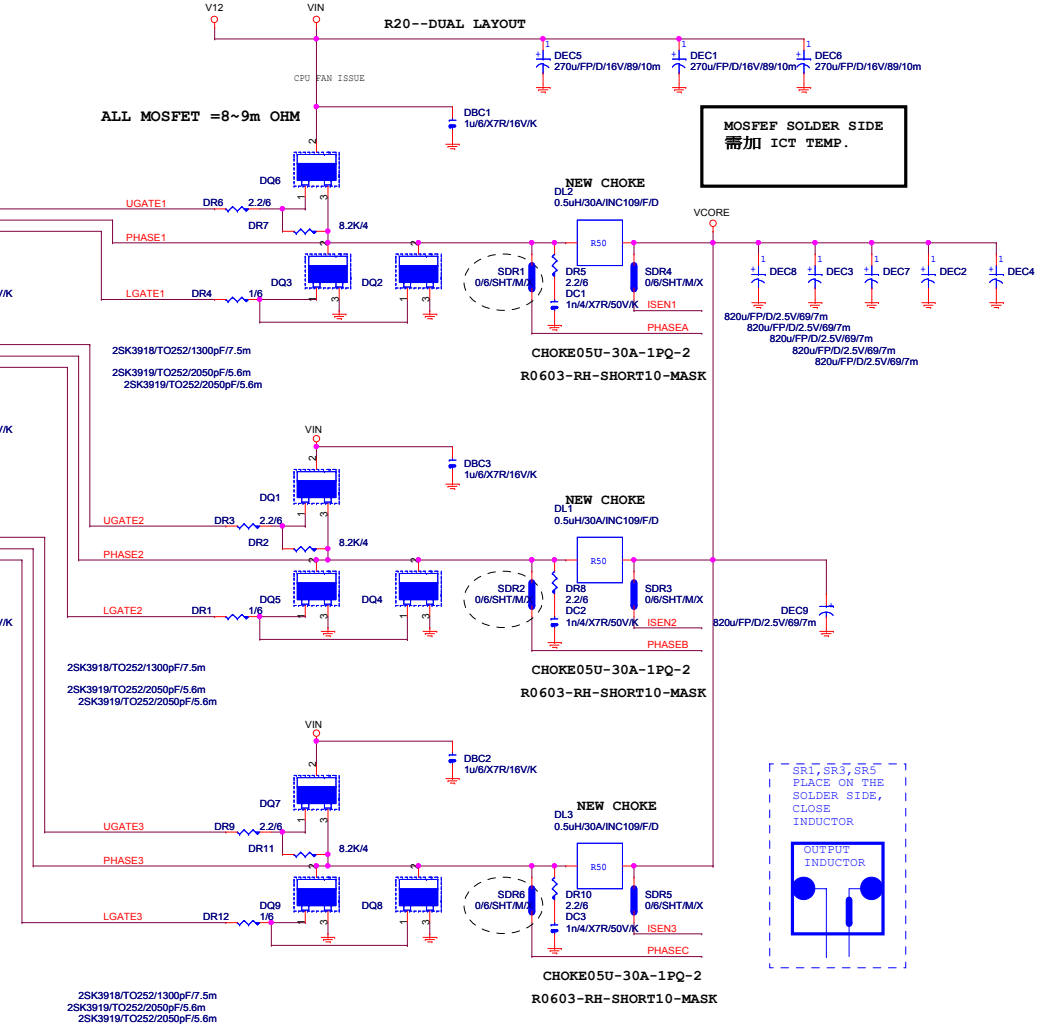
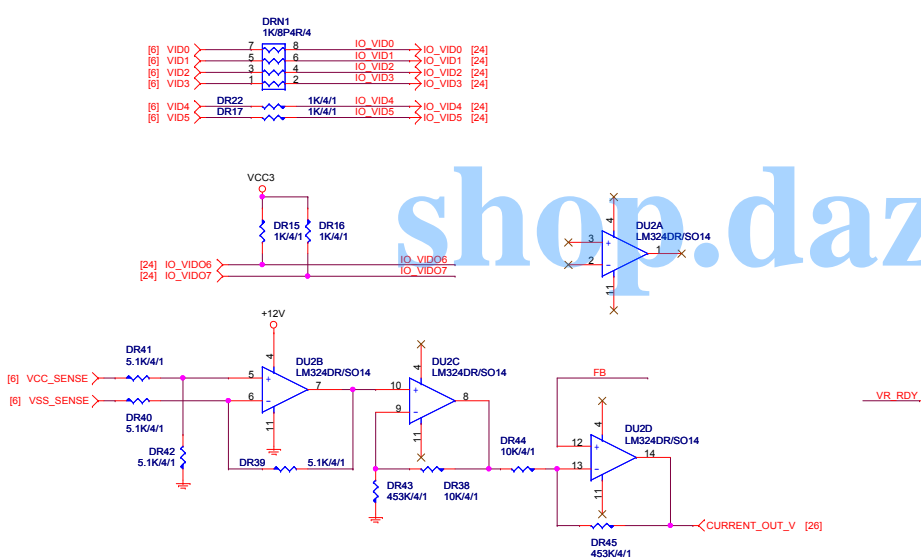
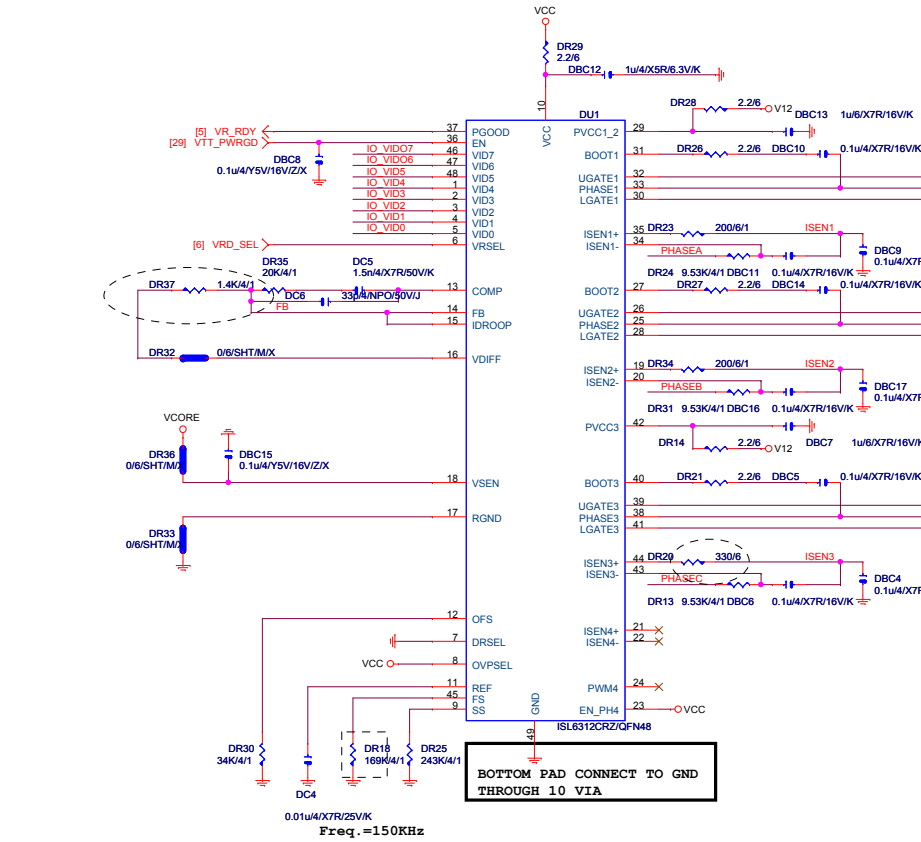


AZALIA FRONT PANEL



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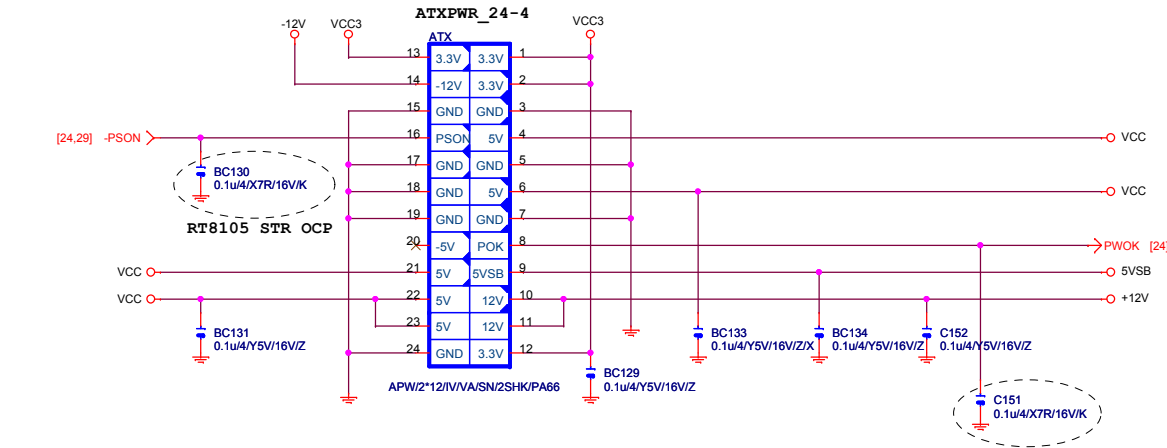




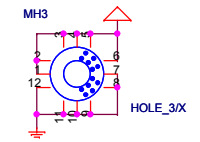
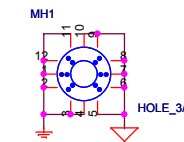
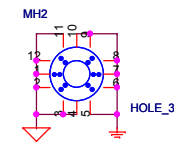
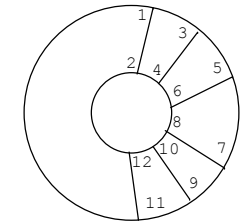
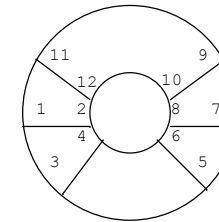
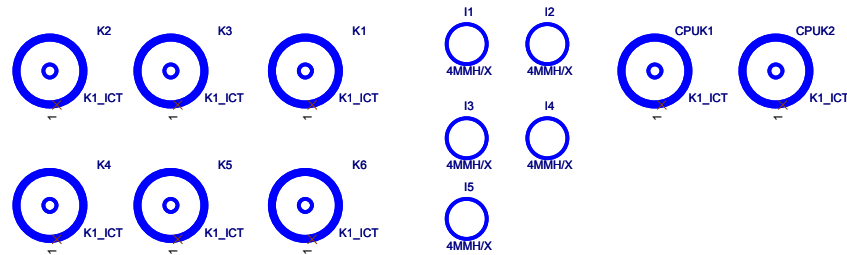
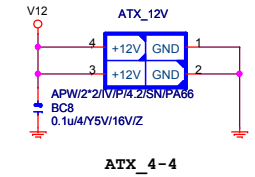
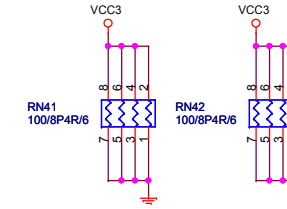
MOSFET SOLDER SIDE
需加 ICT TEMP.

SR1, SR3, SR5
PLACE ON THE
SOLDER SIDE,
CLOSE
INDUCTOR

ATX POWER CONNECTOR

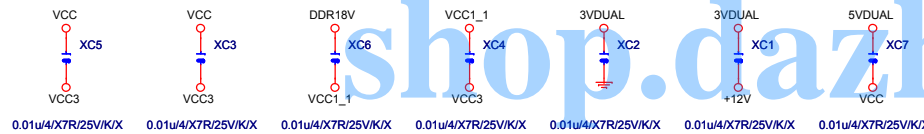
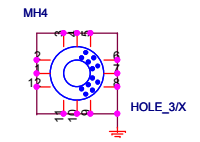
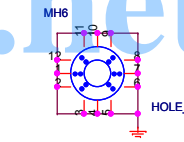
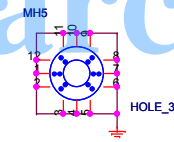


FIX PWR AcBel (ATX-400C-A2ADB)



HOLE_4-RH-1

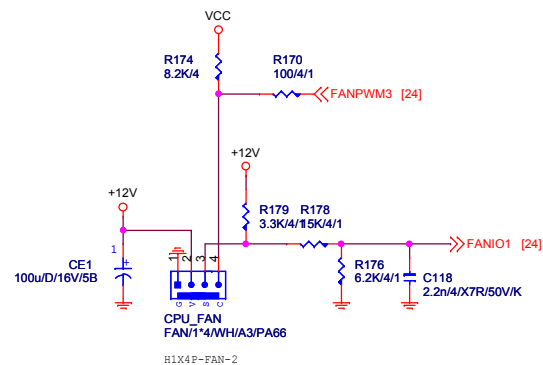
HOLE_4-RH-5MM-1



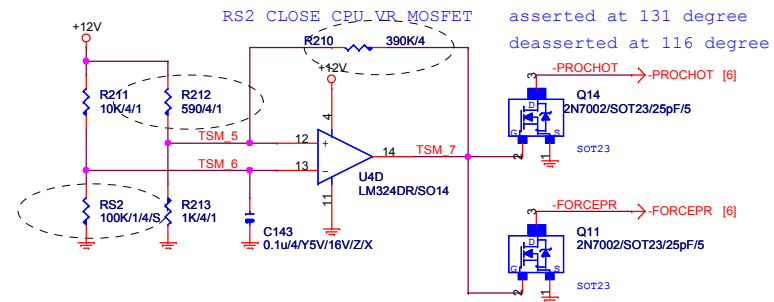
Gigabyte Technology

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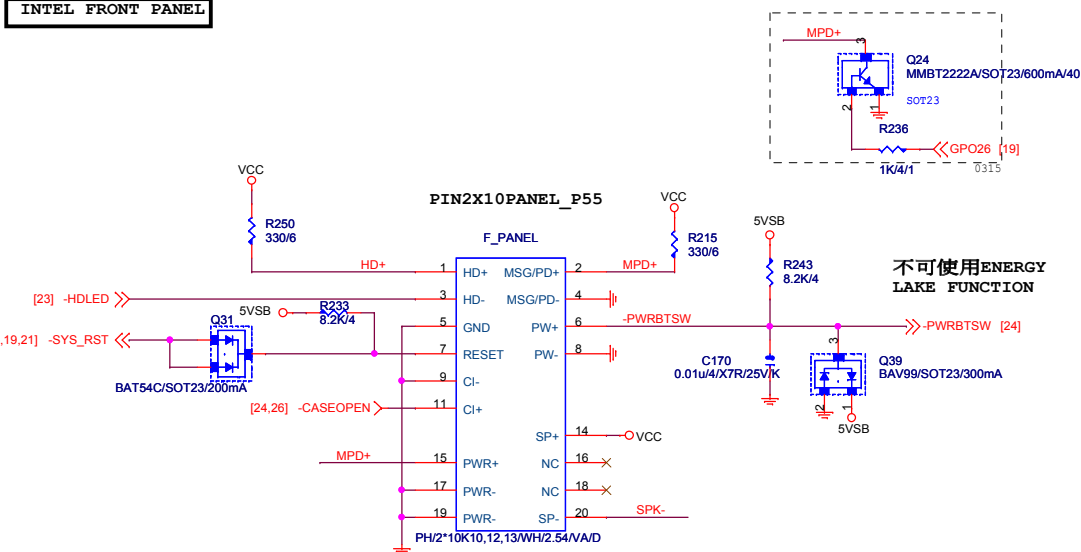
CPU SMART FAN SMART FAN



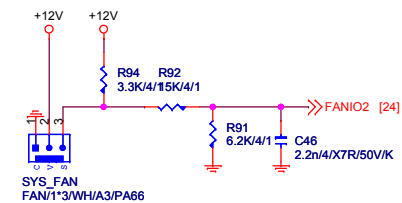
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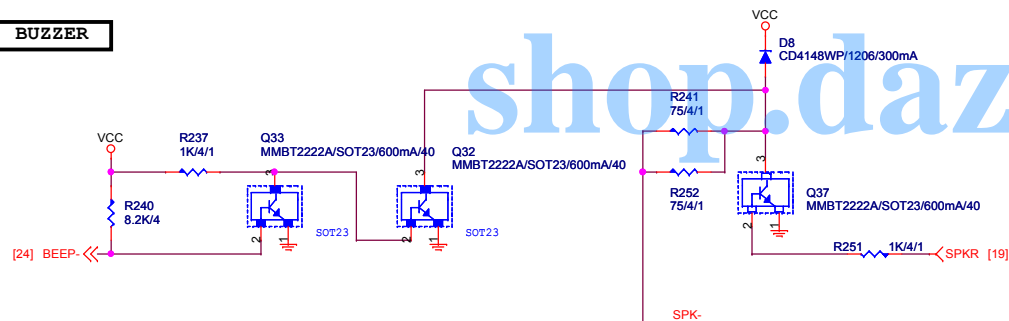
INTEL FRONT PANEL



SYS_FAN

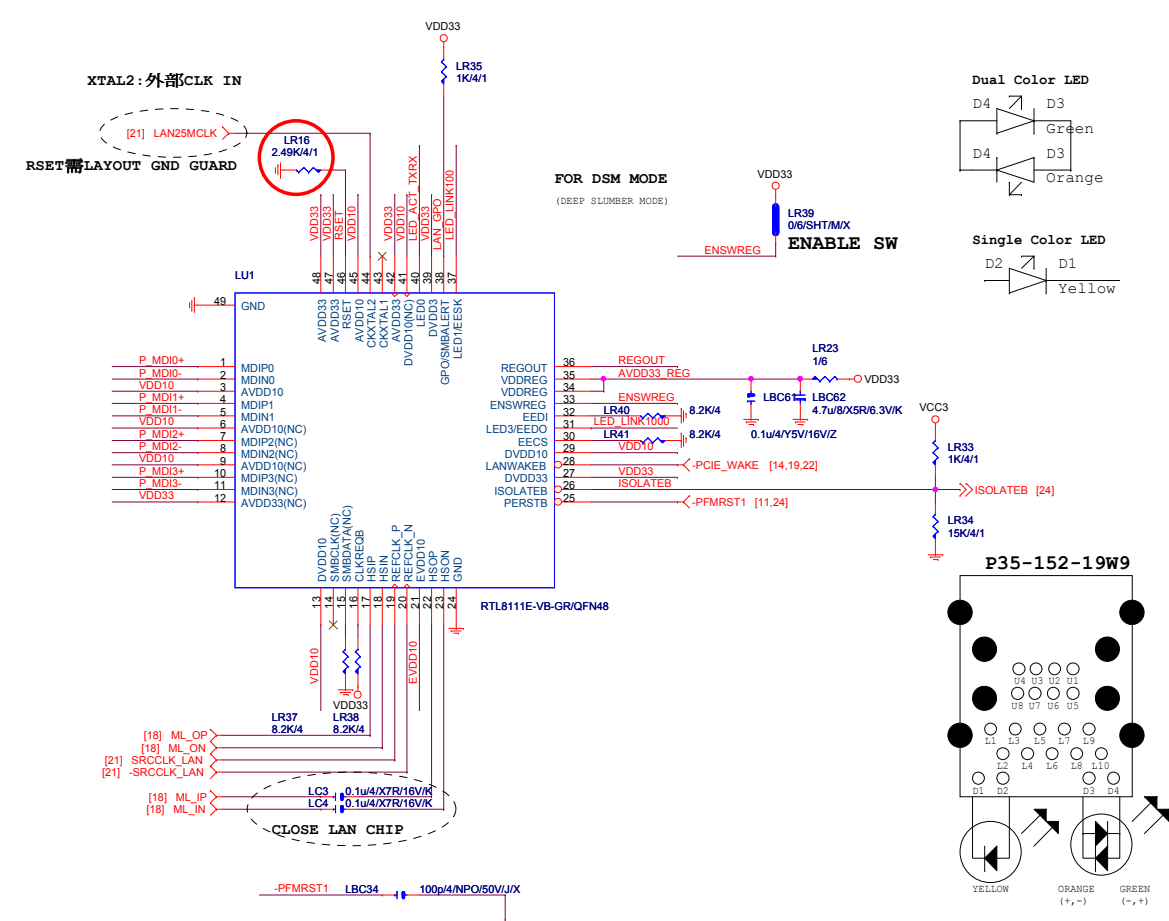


BUZZER

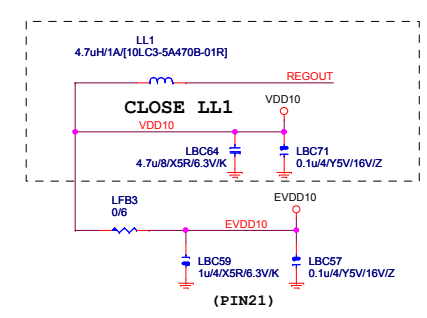
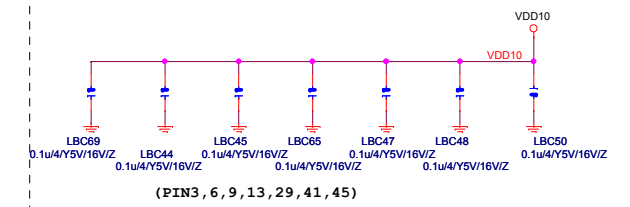
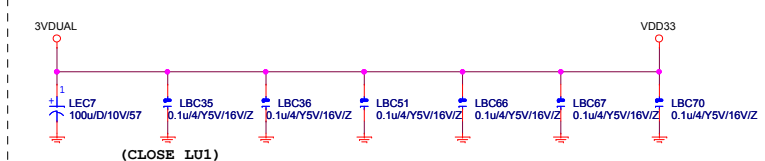
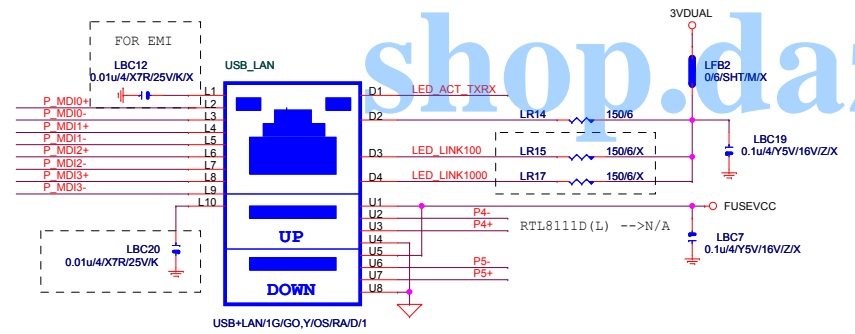


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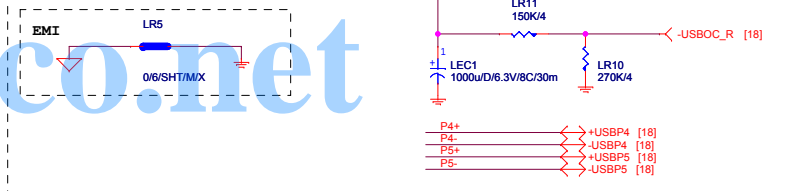
PCIE-1G LAN



USB_LAN CONNECTOR



USB_LAN



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